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U.S. UTILITY Patent Application

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**APPLICATION NO.** 09/887522 SUBCLASS 209 CONT/PRIOR CLASS ART UNIT **EXAMINER** 712 2183 Edward Nevill Andrew Rose

Hardware instruction translation within a processor pipeline

	ISSUING CLASSIFICATION																	
	ORIGINAL CROSS REFERENCE(S)																	
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TERMINAL		DRAWINGS		CLAIMS ALLOWED				
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.			
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subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)					
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not extend beyond the expiration date of U.S Patent. No				ISS	UE FEE			
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this patent have been disclaimed.	(Legal Instrume	ents Examiner)	(Date)					
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